

## **REMARKS**

Claims 4-6 are all the claims pending in the application.

Applicants would like to thank Examiner Robert Chevalier for the courtesies extended to Applicants' representatives during the personal interview conducted on April 6, 2005. During the interview, a general discussion was held regarding the differences between the present invention and prior art of record. In addition, claim amendments were proposed which Applicants believe patentably distinguish the claims over the prior art of record. Applicants note that the claim amendments presented herein are in substantial conformance with the proposed claim amendments discussed during the interview.

### **I. Claim Rejections under 35 U.S.C. § 102**

The Examiner has rejected claims 4-6 under 35 U.S.C. § 102(e) as being anticipated by Mimura et al. (U.S. 6,160,952).

Claim 4, as amended, recites the features of buffering means for storing a system stream, the system stream including video packets and audio packets; and system controlling means for receiving non-seamless-angle information, and for clearing a second part of the system stream remaining in the buffering means after receiving the non-seamless-angle information, wherein the second part of the system stream remaining in the buffering means is cleared by the system controlling means without being demultiplexed by the system decoding means. Applicants respectfully submit that Mimura fails to disclose, suggest or otherwise render obvious at least these features recited in claim 4.

Mimura discloses a reproducing apparatus that includes a RAM section 56, a system processor section 54, a video decoder section 58, an audio decoder section 60, and a sub-picture

decoder section 62 (see Fig. 1). In Mimura, reproduced data stored in the RAM section 56 is processed at the system processor section 54, which separates the data into video data, audio data, and sub-picture data (see col. 10, lines 58-64). This separated data is supplied to the video decoder section 58, the audio decoder section 60, and the sub-picture decoder section 62, respectively, and the data is decoded at the respective decoders (see col. 10, lines 64-67).

Mimura also discloses an angle change operation which can be input from a key/display section 4 or a remote controller 5 (see col. 46, lines 43-45). In particular, Mimura discloses that the angle change operation includes stopping a system time clock (STC) in each section of the reproducing system and clearing the buffers in the video decoder section 58, the audio decoder section 60, and the sub-picture decoding section 62 in order to make it possible to take in the angle data already changed (see Fig. 1 and col. 47, lines 3-7).

Thus, based on the foregoing description of Mimura, it is clear that the buffers of the video decoder section 58, audio decoder section 60 and sub-picture decoder section 62 each hold data that has already been separated by the system processor section 54. For example, the buffer of the video decoder section 58 includes video data, but does not include audio data or sub-picture data. Accordingly, in Mimura, while the buffers of the video decoder section 58, audio decoder section 60, and sub-picture decoder section 60 can be cleared, the data that is cleared from each of these buffers is data that has already been separated into video data, audio data, and sub-picture data.

In view of the foregoing, Applicants respectfully submit that Mimura fails to disclose, suggest or otherwise render obvious the above-noted features recited in claim 4. In particular, as discussed above, because Mimura discloses clearing individual buffers that hold data which has already been separated into video data, audio data, and sub-picture data, Applicants respectfully

submit that Mimura does not disclose or suggest the features of a buffering means for storing a system stream, the system stream including video packets and audio packets; and system controlling means for clearing a second part of the system stream remaining in the buffering means after receiving non-seamless-angle information, wherein the second part of the system stream remaining in the buffering means is cleared by the system controlling means without being demultiplexed by the system decoding means, as recited in claim 4.

Accordingly, Applicants respectfully submit that claim 4 is patentable over the cited prior art, an indication of which is kindly requested.

Regarding claim 5, Applicants submit that this claim is patentable over Mimura for at least similar reasons as discussed above with respect to claim 4. In particular, Applicants respectfully submit that Mimura fails to disclose, suggest or otherwise render obvious at least the features of storing a system stream in a buffer, the system stream including video packets and audio packets; receiving non-seamless-angle information at a system controller; and clearing a second part of the system stream remaining in the buffer after the system controller receives the non-seamless-angle information, wherein the second part of the system stream remaining in the buffer is cleared without being demultiplexed.

Accordingly, Applicants respectfully submit that claim 5 is patentable over the cited prior art, an indication of which is kindly requested.

Regarding claim 6, Applicants submit that this claim is patentable over Mimura for at least similar reasons as discussed above with respect to claim 4. In particular, Applicants respectfully submit that Mimura fails to disclose, suggest or otherwise render obvious at least the features of a buffer operable to store a system stream, the system stream including video packets and audio packets; a system controller operable to receive non-seamless-angle information, and

operable to clear a second part of the system stream remaining in the buffer after receiving the non-seamless-angle information, wherein the second part of the system stream remaining in the buffer is cleared by the system controller without being demultiplexed by the system decoder.

Accordingly, Applicants respectfully submit that claim 6 is patentable over the cited prior art, an indication of which is kindly requested.

## **II. Conclusion**

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may best be resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

Respectfully submitted,

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